



21867

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Juan G. Revilla, et al.
Serial No. : 10/040,920
Filed : December 28, 2001
Title : METHOD FOR EXTENDING THE LOCAL MEMORY ADDRESS SPACE OF
A PROCESSOR

Art Unit : 2186
Examiner : Tuan V. Thai
Assignee : Intel Corporation

RECEIVED

DEC 19 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Technology Center 2100

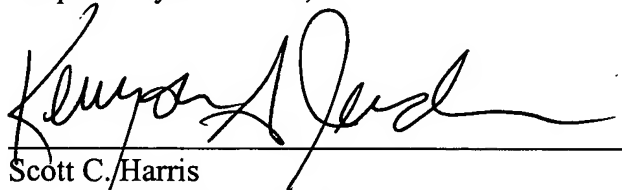
TRANSMITTAL LETTER

Correspondence relating to this application is enclosed, i.e., an Amendment and Replacement Drawing Sheet. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	32	-	31	=	1	\$18
Independent	9	-	8	=	1	\$86
TOTAL FEE DUE						\$104

A check for \$104 is attached.

Respectfully submitted,



Scott C. Harris
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BY
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Date: December 15, 2003

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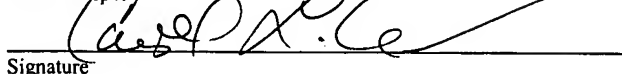
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 15, 2003

Date of Deposit



Signature

Carol Allman

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